



The Global Leader in Specialized Storage and Memory Solutions



KEY FEATURES

- 2X DDR4 speed
- PMIC for efficient power management On-die ECC
- 1.1V lower power consumption • Up to 128 Gb Density with 4-Layer TSV
- RDIMM with precise temperature control

ATP's DDR5 solutions are expected to deliver performance and reliability improvements over the previous generation, especially for critical computing applications.

As the next-generation DRAM specification, DDR5 is poised to exceed DDR4 in every way. DDR5 promises faster performance, higher memory bandwidth, higher densities, and a new power management structure that delivers better power efficiency. All of these advantages, and more, are expected to meet the ever-growing memory needs of present and future applications.

Both DDR4 and DDR5 dual-inline memory modules (DIMMs) still have 288 pins, but with DDR5's higher bandwidth, this means it can transmit data faster. While the pin count is the same, DDR5 DIMMs will not fit in DDR4 sockets as the alignment key is located differently and the pinouts have been changed to accommodate the new features.

Technologies	30µ″ Golden Finger	ATP TDBI	Wide Temperature	Anti-Sulfur Resistors	Conformal Coating	PCB Chamfer	
RDIMM	•	•	A	A	-	A	
ECC CUDIMM	•	•	A			A	
Non-ECC CUDIMM		•				A	
ECC CSO-DIMM	•	•	A	A	A	A	
Non-ECC CSO-DIMM		•				▲	
RDIMM	•	•			-	A	
ECC UDIMM	•	•				A	
Non-ECC UDIMM	A	•	A		A	A	
ECC SO-DIMM	•	•				A	
Non-ECC SO-DIMM		•	A		A	A	

Specifications

DDR5												
DIMM Type				Non-ECC UDIMM								
Density	16 GB to 256 GB	16 GB to 64 GB		8 GB to 64 GB		16 GB to 64 GB		8 GB to 64 GB				
Speed up to (MT/s)*	6400*	5600	6400*	5600	6400*	5600	6400*	5600	6400*			
PCB Height	Low profile / VLP**	Low profile / VLP**		Low profile		Low profile		Low profile				
Operating Temperature				0°C to 85°C /	-40°C to 85°C		×0 - 1-1		**\// D 0 7/ "			

** VLP: 0.74 *Available in 2H 2025



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Product spec and its related information are subject to change without advance notice.

Please refer to www.atpinc.com for latest information

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